
Advertise towards the position of Project Associate (JRF)

Project Associate (Junior Research Fellow) in DST-Funded project

Potential candidates are invited to submit their Application/Resume for the position of Project Associate (JRF) in DST-ICPS division Funded Projects. There are two different projects in this advertisement and are represented as Post-A and Post-B.

Post A: Project Associate (JRF – 1 Position) in the RF and Microwave Area

Project Title: Design of Self sustainable RF circuit using RF and Solar energy harvesting.

Job profile: The JRF will be involved in the design of antenna and passive RF circuit required for RF and solar energy harvesting.

B. Tech or equivalent degree with 70% marks or 7 CGPS in Electronics and Communications /Electrical and Electronics. Valid GATE score is necessary for candidates who holds UG -Degree Only

Or

Eligibility:

M.TECH/MS/ME in one of the areas: Communications and Signal Processing/ Communications/ Electronics/Instrumentation/RF and Microwaves with 70% marks or 7 CGPA

Selection criteria for Post A:

Essential: Sound Knowledge of Electromagnetic Theory and antenna design. Knowledge of Electromagnetic simulation software and Matlab.

Desirable: Candidate with experience in RF and Microwave related projects during their UG or PG curriculum

Fellowship: 25,000/- per Month for period of one year. There is no provision of HRA in this project. A suitable accommodation will be provided upon payment of monthly fee. Accommodation is subjected to availability and as per institute norms.

Duration: Initially for 1 year (May be extended subject to the performance, project requirement and funding). The selected candidate will need to join the project immediately. Termination of the JRF position will be subjected to on one-month notice period from either side.

Post B: Project Associate (JRF – 2 Positions) in the Digital VLSI Architecture & System Design for Wireless Communication System.

Project Title: High-Throughput & Energy-Efficient Flexible-Turbo/LDPC Decoder for the Next-Generation Wireless-Communication Systems.

Job profile: The JRF will be basically working in physical layer of 5G wireless standard. Designing digital architecture and VLSI algorithms for contemporary channel decoders for LDPC/Polar codes. JRF will work in wide range from algorithm design to architecture construction, FPGA prototyping and eventually ASIC chip tape-out in latest nm-CMOS process. JRF will be exposed to VLSI EDA tools from Cadence and Synopsys.

B. Tech or equivalent degree with 70% marks or 7 CGPS in Electronics and Communications / Electrical and Electronics. Valid GATE score is necessary for candidates who just have UG degree.

Or

Eligibility: M.TECH/MS/ME in VLSI with 70% marks or 7 CGPA.

Selection criteria for Post B:

Essential: Basic understanding of wireless communication systems including Antenna, RF-module, and especially digital-baseband module. Preferably, sound knowledge of channel encoding and decoding processes.

He/she must have strong background of digital logic design, digital VLSI architectures, VLSI digital-signal-processing, FPGA prototyping, and digital integrated-circuits. Candidate must be familiar with MATLAB environment and well acquainted with HDL language like Verilog (preferably) or VHDL.

Desirable: Candidate may have experience or did VLSI digital design related projects for wireless communication applications in their UG or PG curriculum.

Fellowship: Rs. 25,000/- per Month for period of one year. There is no provision of HRA in this project. A suitable accommodation will be provided upon payment of monthly fee. Accommodation is subjected to availability and per institute norms.

Duration: Initially for 1 year (May be extended subject to the performance, project requirement and funding). The selected candidate will need to join the project immediately. Termination of the JRF position will be subjected to on one month notice period from either side.

How to apply: Candidate should send their resume to the mail – ID given in the end of this advertisement. The resume should have following points clearly mentioned:

1. Contact Details: Working Mobile number, valid mail ID and skype ID
2. B.TECH/BE percentage or CGPA, Date of graduation, information of College and Affiliated University/Institute
3. M.TECH/ME/MS percentage or CGPA, Date of graduation, with information of College and Affiliated University/Institute
4. Project undertaken during UG and PG with short (half page abstract).
5. Valid gate score if applicable
6. Knowledge of software or techniques acquired which may be needed for this project
7. Personal information: Date of Birth, Martial Status,
8. Job Experience if any
9. Reference: At least two

All above information should be in a single .pdf file duly signed by the candidate in the end.

Important Date:

1. Last date of resume submission: 2nd June 2019
2. Short listing and Information decimation to shortlisted candidate to be called for interview: 4th June 2019
(No information or communication will be made with candidates who are not shortlisted.
3. Date of interview: 19th June 2019:
(Students should carry all documents in original to support their resume).

Note: Those who wish to have skype interview should mention this as a first line in their resume stating: “Opting for skype Interview”. Project investigator will not be responsible for any miss communication/connectivity issue during skype session. Schedule of skype session once decided will not be changes. It will be responsibility of the candidate to ensure all that he/she is well connected and present during allotted slot. The interview committee will call the student during their allotted slot.

Contact ID : Candidate Applying can send their resume in prescribed format to gopishrikanth@gmail.com for Post-A; & shrestha0727@gmail.com for Post-B.

How to reach: http://iitmandi.ac.in/institute/campus/how_to_reach.php
http://iitmandi.ac.in/files/inst_bus_schdl_21aug2017.pdf