

SRIC/06

INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE
(Name of Dept./Centre)

Dated: 01/05/2019

ADVERTISEMENT TO FILL UP PROJECT POSITIONS*

Applications are invited from Indian nationals only for project position(s) as per the details given below for the consultancy/research project(s) under the Principal investigator (Name: **Dr. Bhaveshkumar Bhalja**), Department of Electrical Engineering, Indian Institute of Technology, Roorkee.

1. Title of project: **Design, Development and Validation of a New Adaptive Digital Relaying Scheme for Power Transformer**
 2. Sponsor of the project: **CPRI, Bangalore**
 3. Project position(s) and number: **01 JRF**
 4. Qualifications: **M. Tech./M. E. Electrical (Specialization in Power System). Students of last semester may also apply (However, he/she has to submit provisional degree certificate at the time of joining, if selected).**
 5. Emoluments: **25,000 + HRA (as per institute rules)**
 6. Duration: **1.5 Years (up to project duration)**
 7. Job description:
 - **Development of a laboratory prototype for testing of various types of faults on custom design transformer and acquiring various signals for testing of developed algorithm.**
 - **Development of an algorithm for internal fault discrimination and classification incepted on transformer winding using acquired signals.**
 - **Compiling the results, data interpretation, writing research paper and project reports.**
1. Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply.
 2. Candidates desiring to appear for the Interview should submit their applications with the following documents to the office of Principal Investigator through email, by post or produce at the time of Interview:
 - Application in a plain paper with detailed CV including chronological discipline of degree/certificates obtained.
 - Experience including research, industrial field, and others.
 - Attested copies of degree/certificate and experience certificate.
 3. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of interview for verification.
 4. Preference will be given to SC/ST candidates on equal qualifications and experience.
 5. Please note that no TA/DA is admissible for attending the interview.

Note: The selected candidate may get an opportunity for Ph. D. admission.

The interview will be held at **Committee Room, Department of Electrical Engineering, IIT Roorkee on May 20 (Monday), 2019 at 11:30 AM.**

TEL: +91-1332-284804 (O)
Email: brbl4fee@iitr.ac.in

ESB/mj
01/05/2019
Dr. Bhavesh Bhalja
Principal Investigator

*To be uploaded on IIT Roorkee website and copy may be sent to appropriate addresses by PI for wider circulation.

Approved
WSD
कुलपति (वि.वि.)/Dean (SRIC)
2.5.19