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DEAN SRIC OFFICE IIT ROORKEE
Dist. No. 7952 Dated 18/10/19
Sent to 54 4/25
Supdt. A.R. (Admn.)

**SRIC/06**

DST-1400-ECD

**INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE**
**Electronics & Communication Engineering Department**

Dated: 17.10.2019

## ADVERTISEMENT TO FILL UP PROJECT POSITIONS*

Applications are invited from Indian nationals only for project position(s) as per the details given below for the consultancy/research project(s) under the Principal investigator (Name: **Dr. Anand Bulusu**), Dept./Centre **Electronics & Communication Engineering**, Indian Institute of Technology, Roorkee.

1. Title of project: : Development and Efficient Characterization of Floating Body (FB) and Dynamic Threshold (DT) CMOS Partially Depleted Silicon-on-Insulator (PDSOI) Standard Cell Libraries

2. Project position(s) and number : RA-I (No. 02)
1. Qualifications : Ph.D/M.Tech in Microelectronics or equivalent with 3 years exerience after M.Tech
2. Emoluments : 47000/- + HRA
3. Duration : One year extendable upto project completion
4. Job description : Transistor Level Circuit Design of Digital CMOS Circuits, Semiconductor Device Physics, TCAD process/device simulations. Layout and parasitic extraction, SPICE.

1. Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply.
2. Candidates desiring to appear for the Interview should submit their applications with the following documents to the office of Principal Investigator through email, by post or produce at the time of Interview:
   - Application in a plain paper with detailed CV including chronological discipline of degree/certificates obtained.
   - Experience including research, industrial field and others.
   - Attested copies of degree/certificate and experience certificate.
3. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of interview for verification.
4. Preference will be given to SC/ST candidates on equal qualifications and experience.
5. Please note that no TA/DA is admissible for attending the interview.

The last date for application to be submitted to office of Principal Investigator is 05/11/19 by 5 PM.
(*not applicable for walk in interview*)

The interview will be held at [illegible] on [illegible] at [illegible]. (to be given only for walk in interview)

B. Anand
(Anand Bulusu).

**Tel: 01332-285347** **Fax:**

**Name and signature of Principal Investigator**

**Email: anandfec@iitr.ac.in**

*To be uploaded on IIT Roorkee website and copy may be sent to appropriate addresses by PI for wider circulation.

Please contact navenmce@gmail.com for queries.

Approved as modified

21/10/19

कुलशासक (सिरिक)/Dean (SRIC)

21.10.19