



J.C. Bose University of Science & Technology, YMCA, Faridabad
(A Haryana State Government University)

(Established by Haryana State Legislative Act No. 21 of 2008 & Recognized by UGC Act 1956 w/s 22 to Confer Degrees)

Accredited 'A' Grade by NAAC



DEPARTMENT OF ELECTRONICS ENGINEERING

Dated: 19.10.2022

WALK IN INTERVIEW

Eligible candidates are invited to attend Walk -in- Interview for engagement of Guest Faculty in Department of Electronics Engineering, J. C. Bose University of Science and Technology, YMCA, Faridabad.

Sr. No.	Name of Discipline	Number of Vacancies	Minimum Qualification	Remuneration
01	Electronics Engineering	04	B.E. / B.Tech. / and M.E. / M.Tech. / in relevant branch with First Class or equivalent either in BE/B.Tech or ME/M.Tech.	Rs. 1500/- per hour with a ceiling of Rs. 50000/- per month
VENUE OF THE INTERVIEW			1 ST FLOOR, CONFERENCE ROOM, ELECTRONICS ENGINEERING DEPARTMENT	
DATE OF INTERVIEW			31-10-2022	
REGISTRATION TIME			09:30 AM to 10:00 AM	
INTERVIEW TIME			12:30 PM onwards	

Interested candidates who are eligible as per AICTE norms may attend the interview. The candidate should bring his/her bio- data along with the copy of the publications and photocopies of their certificates along with original and two recent colour photographs.

Note: No TA/DA shall be paid for this purpose. The Selected candidates will have to join immediately.

Chairperson
Deptt. of Electronics Engineering
J.C. Bose University of Science
and Technology, YMCA, Faridabad
19/10/22

CC:

PA to Vice Chancellor - for information

Website manager- with the request to upload the same on university website.