

राष्ट्रीय प्रौधोगिकी संस्थान गोवा NATIONAL INSTITUTE OF TECHNOLOGY GOA

Farmagudi, Ponda, Goa - 403 401, India

Department of Electrical and Electronics Engineering

Advt. No.: NITG/EEE/ADVT/2023/002, Dated: 12th May 2023

Advertisement for the Position of Junior Research Fellow under the Project DST/SERB/EEQ/2021/000294

Applications are invited from interested and highly motivated candidates for the post of Junior Research Fellow (JRF) to work on the R&D project titled "Development of Metaheuristic Algorithms based Maximum Power Point Tracking Controllers for various Grid-Connected/Stand-Alone PV System to Enhance the Maximum Power Generation Capability", has been sanctioned by Science and Engineering Research Board (SERB), Dept. of Science and Technology (DST), Govt. of India. JRF will be appointed initially for one year (on contract) and his/her services will be extended for subsequent year(s) based on the performance review. The position is coterminus with the project.

S.No	Particulars	Details	
1.	Post/Position	Junior Research Fellow (JRF)	
2.	Title of the Project	Development of Metaheuristic Algorithms based Maximum Power Point Tracking Controllers for various Grid-Connected/Stand-Alone PV System to Enhance the Maximum Power Generation Capability	
3.	Sponsoring Agency	Science and Engineering Research Board (SERB), Dept. of Science and Technology (DST), Govt. of India	
4.	Duration of the Project	3 Years (2022-2025)	
5.	No. of positions	01 (One)	
6.	Salary/Emoluments	Rs. 31,000/- pm (for the first 2 years) and Rs. 35,000/- pm (for the 3rd year) plus House Rent Allowance (HRA) as per the Institute norms	
7.	Principal Investigator	Dr. Suresh Mikkili	
8.	Department	Electrical and Electronics Engineering	
9.	Essential Qualification	M.Tech./M.E. in relevant specializations of Electrical Engineering/Electrical and Electronics Engineering with minimum 60% marks (or a minimum CGPA of 6.5 on a 10-point scale) and with a GATE Qualified.	
10.	Desirable Qualification	A strong knowledge of MATLAB-Simulink is highly desirable. Additionally, knowledge in Grid-tied PV System, MPP Techniques, Optimization Techniques, Multi-level Inverters and FPGA/dSPACE/DSP Controller programming would be appreciated.	
11.	No Objection Certificate	Applicants in employment (private, government or any other organization) are required to submit a "No Objection Certificate" from the employer at the time of interview.	
12.	Last date of receiving filled up application form	05 th June 2023	

13.	Relaxation on qualification and age	SC/ST/OBC/EWS/WOMEN/PWD candidates will get relaxation as per Government of India Rules and Regulations.	
14.	Application Process	Application form (as given below) giving all the details and attested copies of certificates and supporting documents should reach to Dr. Suresh Mikkili (PI) at below given address latest by <u>05th June 2023</u> in Hardcopy or the duly filled and signed scan copy of Application Form along with the scanned copies of mark sheets/relevant documents can be sent to PI <u>through an e-mail</u> , at: <u>mikkili.suresh@nitgoa.ac.in</u> , with subject line " <u>Application for the post of JRF in the Department of EEE under DST/SERB/EEQ/2021/000294"</u> . Call letter for shortlisted candidates will be sent through an email.	
15.	Other Benefits	The selected candidate is eligible to register for Ph.D. programme at NIT Goa as per the Institute Norms. However, s(he) needs to apply for Ph.D. position when the advertisement is open.	

Important Instructions:

- Candidate possessing the requisite qualification and experience should apply; in the attached format along with their updated CV latest by <u>05th June 2023</u>. The applicant will be responsible for the authenticity of information, other documents and photographs submitted.
- 2. Mere, possessing the prescribed qualification does not ensure that the candidate would be called for Interview. The Candidates will be shortlisted on the basis of merit and need of the project.
- 3. The Shortlisted Candidates will be informed by e-mail (apart from website) along with the date and time of the written test/interview. No other letter will be sent to the correspondence address. So, the candidates are advised to check their email regularly.
- 4. Shortlisted candidates have to present themselves for the interview on the interview date with updated CV, application form, original and attested photocopies of mark sheets/certificates in support of their academic qualifications.
- 5. The written test/interview will be conducted for all the eligible candidates.
- 6. No TA/DA shall be paid to candidates for attending the Interview and/or joining the position.
- 7. The appointment is for time bound project and the candidate is required to work dedicatedly for the successful completion of the project. Selected candidate has to join immediately.
- 8. Incomplete application forms and forms received after due date will be summarily rejected.
- 9. All the Terms and Conditions for this recruitment will be as per guidelines of DST SERB, Govt. of India.

Address for Correspondence

Dr. Suresh Mikkili, Associate Professor, Electrical and Electronics Engineering, National Institute of Technology Goa, Farmagudi, Ponda, 403401, Goa- India. Telephone: +917588133009, 0832-2404214

Email: mikkili.suresh@nitgoa.ac.in

Website: www.nitgoa.ac.in

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Application for the post of JRF in the Dept. of EEE under DST/SERB/EEQ/2021/000294

1. Post Applied for : Junior Research Fellow (JRF)

Title of Project: Development of Metaheuristic Algorithms based Maximum Power Point Tracking Controllers for various Grid-Connected/Stand-Alone PV System to Enhance the Maximum Power Generation Capability

2. Name of the Car	Pa	Paste here a				
3. Father's Name (Pa	recent ssport size				
4. Mother's Name	Pl	hotograph				
5. (a) Date of Birth: (DD/MM/YYYY)						
(b) Sex (Male	(b) Sex (Male/Female/Other):					
(c) Marital Sta	(c) Marital Status (Married/Single):					
(d) Category ((d) Category (SC/ST/OBC/PWD/GEN):					
6. Previous Resear	6. Previous Research experience, if any:					
7. Publication(s), i	7. Publication(s), if any:					
8. GATE : Qualified (Yes/No): Score:Rank:						
Special	ization:	Year:	_ Year:			
9. Academic Quali	fication: (Starting from Standard 10	or equivalent E	Examination)			
Name of the Exam Passed	Name of the School/College/Institute/ University	Year of Passing	Discipline/ Specialization	Percentage of Marks/ CGPA		



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10. (a) Address for Communicati	on: (BLOCK LETTER)	
(b) Contact No. (Mobile)		
(c) E-mail ID:		
11. Contact Details of two refere	ees:	
	Referee -I	Referee- II
Name:		
Designation:		
Organization:		
Office Address:		
Phone Number:		
Email ID:		
12. Experience details:		
		n is true to the best of my knowledge
and belief. If selected, I promise t	to abide by the rules and regulation	s of the Institute.
Date:		
Place:		Signature of the candidate
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