



Research and Development Section
Indian Institute of Technology Guwahati
Guwahati-781039, Assam

Applications are invited for **Walk in interview** for the following post in the project entitled, "Securing Low Power Embedded Processors for IoT Applications Against Power-Analysis Attacks" at the Department of Electronics and Electrical Engineering, IIT Guwahati

Date: 05.07.2023 (Wednesday)

Time: 11 AM onwards

Venue: EEE Conference Room

Sl. No	Project Staff Designation	Number of Vacancies	Pay Recommended (Rs.)	HRA Required (Rs.)	Medical Required (Rs.)	Total Amount (Rs.)	Duration of Appointment in months	Qualifications
1	Associate Project Engineer	1	35000	6300	1250	42550	5	Master's degree in ECE/EEE/Electronics and Instrumentation Eng or Bachlor's degree in ECE/EEE + 3 years' experience in Hardware Security. (If suitable candidates are not available for Associate Project Engineer, we may recruit Assistant Project Engineer with experience in Hardware Security)

How to apply and selection process: Candidates have to appear for the Walk-in-interview. They have to send application/CV giving details of all educational qualifications (starting from 10th onwards), experience, contact address, phone no., e-mail etc. By 5th July 2023 at 5 PM to the PI at the following email address. Shortlisted candidates will be called for the interview through e-mail. Selection will be based on the performance of the candidate in the interview.

For any further clarification, contact: Prof. Roy Paily Palathinkal (Principal Investigator)

Email: roypaily@iitg.ac.in

Phone: 0361-258-2512

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Asstt. Registrar (R&D)