



भारतीय प्रौद्योगिकी संस्थान इंदौर

खंडवा रोड, इंदौर 453 552

Indian Institute of Technology Indore

Office: +917316603292

IIT Indore

**Advertisement for Junior Research Fellow (JRF) Position under CSIR
Funded Project**

Date: 17th November 2023

Applications are invited from interested and motivated candidates for the post of Junior Research Fellow (JRF) in a time bound research project for a temporary period, purely on contractual basis as per the following details

Position	Junior Research Fellow (JRF)
Number of Vacancy	1
Project Title	Blind Reconstruction of Channel Encoder and Interleaver for Future Generation Communication System
Department	Electrical Engineering
Project Tenure	3 Years and/or co-terminus with the project
Job Description	The scope of the project is to propose blind estimation algorithms for reconstruction of channel encoders and interleavers. These algorithms play a significant role in the design of intelligent or blind receivers for future generation/military communication systems. Strong background in Wireless/Digital Communications, Signal Processing for Communications, and Coding Theory, good MATLAB/PYTHON coding skills, and knowledge on USRP-software-defined radio (SDR) systems are desirable
Essential Qualification	B.S. 4 years programme / Integrated B.S.-M.S. / M.Sc. / B.E. / B. Tech. or equivalent degree, with atleast 55% marks and passing of NET-LS/GATE test. For further details Pl. refer to CSIR-HRDG website: www.csirhrdg.res.in
Age Limit	JRF – 28 Years
Age Relaxation	The upper age limit is relaxable up to 5-years in the case of candidates belonging to scheduled castes/tribes/OBC, women and physically handicapped candidates
Fellowship	As per CSIR Norms (visit csirhrdg.res.in)



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Interested candidates are requested to submit the application form in the following google form link:
<https://forms.gle/w9TLjPbh5JCpcxrf6>

Last Date for Applying: 03/12/2023 (Sunday)

Only shortlisted candidates will be called for interview. Selected candidates and the interview schedule will be intimated by email. No TA/DA will be paid for appearing in the interview. The selected candidates will be encouraged to register for PhD program as per institute norms.

In case of any queries, please contact Dr. Swaminathan R (Principal Investigator), Associate Professor, Department of Electrical Engineering, IIT Indore, Contact No. 9384528819, Email: swamiramabadrn@iiti.ac.in

For ongoing research activities please refer to the homepage of Dr. Swaminathan R ([Homepage](#))