No. IIITDMKL/ECE/JRF/2024/01   Date: 12 Feb 2024

Applications are invited for the following temporary post to work on the C2S project (Category III) sponsored by MeitY entitled "Instruction-Data level Parallelism based Hardware Accelerator Design in HPC and CPS Applications".

Name of the Post	Junior Research Fellow
Number of posts	Two
Nature of the post	The post is purely temporary and is for the duration of one year. It may be extended for another one/two year(s) or till the completion of the project depending on the requirement and the performance of the candidate.
Minimum Qualification and Experience	B.E/B.Tech in Electrical/Electronics Engineering with GATE qualified at any time. AND/OR M.E/M.Tech in Microelectronics/VLSI design with GATE qualified at any time.
Desirable Qualification and Experience	Candidate having experience in Digital System Design, RTL design using Verilog/VHDL, IP integration using Xilinx Vivado, Custom IP development using Xilinx Vivado, and Semi Custom ASIC design using EDA tools.
Salary	Rs. 37,000 per month (consolidated)
Ph.D. Opportunity	JRF candidate selected for this position will have the opportunity to register for regular Ph. D. program in IIITDM Kurnool provided JRF satisfies the eligibility criteria for the same.
Mode of Application	The duly filled/signed application along with the copy of the necessary testimonials should be sent by the post to the Principal Investigator of this project. OR The scanned copy of the duly filled/signed application along with the necessary testimonials should be sent by Email to the Chief Investigator (CI) of this project.
Correspondence Address of Chief Investigator	Dr. Mohamed Asan Basiri M, Department of ECE, IIITDM Kurnool, Jagannthagattu, Kurnool, Andhra Pradesh - 518007, Email - asan@iiitk.ac.in
Important Dates	The last date of receipt of the completed application form is 11 March 2024. Mode of selection: Written Test/Interview The date of Written Test/Interview will be informed to the shortlisted candidates by email.

Instructions to the Applicants:

1) The post is purely temporary and is for the duration of one year. It may be extended for one more year depending on the requirement and the performance of the candidate.

2) Only short-listed applicants will be called for the written test followed by an interview.

3) Applicants who do not meet the required qualification will not be called for the interview. The institute reserves the right to fix suitable criteria for short listing of eligible candidates satisfying qualification and experience.

4) No interim correspondence / inquiries will be entertained from the candidates in connection with the process of shortlisting.

5) The candidate is responsible for the correctness of the information provided in the application. If it is found at a later date that any information given in the application is incorrect / false the candidature / appointment is liable to be cancelled / terminated. Incomplete/ partially filled application forms/ application without the supporting documents will not be considered.

6) Applications which are not strictly as per the format given will be summarily rejected.

7) Canvassing in any form will disqualify the candidature of the applicant.

8) No TA/DA will be provided to attend the written test/interview.

9) The original testimonials will be verified at the time of written test/interview.

10) The shortlisted candidates will be intimated through Email.

11) Applicants are requested to look at website of IIITDM Kurnool (www.iiitk.ac.in) from time to time for information, updates etc. Updations, if any will be published on the website only and not in newspaper.

Director

Application for the recruitment of the post "Junior Research Fellow" to work on the C2S, MeitY sponsored research project in the department of Electronics and Communication Engineering, IIITDM Kurnool

1.	Name of the Candidate	
2.	Date of Birth	
3.	Marital Status	SINGLE/MARRIED
4.	Gender	MALE/FEMALE
5.	Nationality	
6.	Community	GEN/OBC/SC/ST (Attach the necessary proof if the category is OBC/SC/ST.)
7.	PwD	YES/NO (If YES, attach the necessary proof.)
8.	Email	
9.	Phone/Mobile Number	
10.	Permanent Address	
11.	Address for Communication	
12.	Particulars of the GATE GATE Score Year of Passing Department	

13. Academic Record

Certificate/Degree	Specialization	Name of the Institute	Name of the Board/ University	Year of Passing	Percentage/CGPA
SSLC (10th)					

HSC (+2)				
B.E/B.Tech				
M.E/M.Tech				

(Attach the necessary proofs of academic qualifications.)

14. Experience (Industry/ Research/ Teaching)

Designation	Name of the Organization	Duration		Nature of Work
		From	To	

(Attach the necessary proofs of experience.)

15. Any Other Information

I declare that the above given information are true to my knowledge and belief. If any discrepancy is observed, my selection may be cancelled at any stage.

Date:
Place: **Candidate's Signature**

Note: Attach all the supporting documents.